

TECH-TIDE



Department of Electronics
and Communication
Engineering

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Message from HoD

Bridging Knowledge and Innovation in ECE



Warm greetings to all students, faculty members, and valued readers,

It gives me immense pleasure to present this edition of “Tech Tide”, a reflection of the activities, achievements, and aspirations of the Electronics and Communication Engineering Department for the period of January to June 2024.

As the boundaries of technology continue to expand, our role as educators and learners becomes more dynamic and demanding. The field of ECE is evolving rapidly with breakthroughs in AI-integrated electronics, next-generation communication systems, IoT, and semiconductor technologies. Our department remains committed to staying ahead of the curve by continuously adapting our academic and co-curricular ecosystem.

Achievements and the Road Ahead

The past six months have been marked by remarkable achievements — from faculty publications and FDPs to student project showcases, internships, and participation in national-level competitions. I am proud of our students and faculty for their unwavering dedication, innovative thinking, and team spirit.

As we look forward, our focus remains on providing industry-aligned learning, encouraging research and development, and building a strong foundation for students to emerge as competent and ethical engineers.

I invite all stakeholders to actively participate in our journey and contribute to making the department a center of excellence in ECE education.

Warm regards,

Dr.V.Balamurugan

Head of the Department

Electronics and Communication Engineering

Institution Vision & Mission

Vision

Grow as a center of learning and research, transforming students to professionals with knowledge, skill, competence, commitment, confidence and ethics to serve the society.

Mission

- **Mission 1**

To impart value-based education and promote curricular, co-curricular and extracurricular activities amongst students through extensive theoretical and practical training by qualified and experienced personnel using state-of-the-art facilities.

- **Mission 2**

To promote research and consultancy for institution development and contribution to the society.

About ECE Department

The Department of Electronics and Communication Engineering (ECE) at ASET continues to be a dynamic center of academic excellence, committed to nurturing innovation, technical expertise, and industry collaboration. With a forward-looking approach, the department aims to produce competent, ethical, and industry-ready engineers by blending strong theoretical foundations with practical exposure.

Our curriculum is strategically designed to cover core areas such as electronics, communication systems, embedded systems, VLSI design, IoT, signal processing, and AI-integrated systems. This is further strengthened through modern laboratories, dedicated research spaces, and access to industry-standard software tools that foster hands-on learning and creative problem-solving.

The department is led by a team of experienced and research-driven faculty members who bring in-depth subject knowledge, mentorship, and a passion for innovation. Students are encouraged to take part in internships, research initiatives, technical forums, and national-level competitions, cultivating a spirit of inquiry and collaboration.

During the January to June 2024 period, the department witnessed remarkable participation in technical workshops, industrial visits, paper presentations, and career-oriented events — all designed to enhance the holistic development of our students.

Graduates from the ECE Department continue to thrive in top-tier organizations, premier academic institutions, and emerging technology sectors such as telecommunications, robotics, AI, space electronics, and consumer technology.

Department Vision & Mission

Vision

The Department of Electronics and Communication Engineering aims to achieve excellence in academics and research and develop globally competent professionals with social commitment.

Mission

- **Mission 1**

To empower the students with the right attitude, knowledge and leadership skills through value based education using modern technologies and innovative teaching pedagogies.

- **Mission 2**

To develop students' personality through participation in extra and co-curricular activities.

Program Educational Objectives (PEOs)

1. Apply the concepts of Electronics and Communication Engineering to provide solutions to the emerging problems in the society.
2. To solve problems of social relevance applying the knowledge of ECE and pursue higher education.
3. Work effectively as individuals and as team members in multidisciplinary projects.
4. Engage in lifelong learning, career enhancement and adapt to changing professional and societal needs.

Program Specific Outcomes (PSOs)

PSO1: Develop electronics-based solutions for real-life challenges integrating entrepreneurship and sustainability.

PSO2: Uphold ethics and values in designing sustainable technologies while embracing lifelong learning for professional growth.

Program Outcomes (POs)

PO 1.Engineering Knowledge: Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.

PO 2. Problem Analysis: Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.

PO 3.Design/Development of Solutions: Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.

PO 4. Conduct Investigations of Complex Problems: Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.

PO 5. Modern Tool Usage: Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.

PO 6. The Engineer and Society: Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.

Program Outcomes (POs)

PO 7. Environment and Sustainability: Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.

PO 8.Ethics: Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.

PO 9. Individual and Team Work: Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.

PO 10. Communication: Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.

PO 11. Project Management and Finance: Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.

PO 12. Life-long Learning: Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

Department of ECE

Faculty Members

The ECE Department takes pride in its team of committed and knowledgeable faculty members, whose guidance and expertise play a vital role in shaping the future of our students.



Dr. Krishna Kumar Kishor
Vice Principal



Dr. Balamurugan V
HoD



Dr. Leesha Paul
Professor



Dr. Aneesh K
Associate Professor



Divya Mohan
Assistant Professor



Vijitha Khan
Assistant Professor



Swetha C
Assistant Professor



Asha Aravind
Assistant Professor



Gayathri P S
Assistant Professor



Abhijith V
Assistant Professor



Sathyan P



Sindhu V. R

FORMATION OF PARENT TEACHERS ASSOCIATION

The Parent–Teachers Association (PTA) was formed at Ahalia School of Engineering and Technology with the objective of fostering effective interaction and collaboration between the college and the parents of students. The association aims to support the academic and overall development of students through constructive engagement and feedback.

As part of the formation process, faculty members were elected to key positions within the association. Dr. V. Balamurugan and Dr. Krishna Kumar Kishor from the Electronics and Communication Engineering (ECE) Department were elected as Executive Members of the PTA.

Their inclusion is expected to enhance communication between faculty and parents, ensuring the shared goal of student success is achieved effectively.



Dr. P.R. Sreemahadevan Pillai
Principal
ASET



Dr. Krishna Kumar Kishor
Vice Principal
ASET



Dr. V. Balamurugan
HoD
ECE



Dr. G. Murugananth
HoD
EEE |



Dr. Gunasekaran S.
HoD
CSE



Dr. Binoy Balan
HoD
S & H



Prof. Prakash D.
HoD
ME



Ms. Kripa K. M.
HoD
CE



Ms. Lakshmi Suresh
Asst. Prof.
EEE



Mrs. Nirmala K
Parent



Mr. Bibin Rosh A.V.
Parent



Mr. Javaprakashan A.
Parent



Mr. Janardhanan
Parent



Mr. Kannan P.S
Parent



Prof. Purushothaman T.V
Parent



Ms. Hameeda Banu K.S
Parent

Setting Arduino lab at Kendriya Vidyalaya Kanjikode

11 January 2024

As part of the PM SHRI (Prime Minister Schools for Rising India) scheme, an Arduino Lab was successfully set up at Kendriya Vidyalaya, Kanjikode with the technical support of Ahalia School of Engineering and Technology.

Mr. Sathyan P and Ms. Sindhu V R, Senior Lab Instructors at Ahalia School of Engineering and Technology, played a key role in the establishment of the lab. Their expertise and hands-on assistance were instrumental in setting up the equipment and ensuring the lab was ready for student use.

This initiative is expected to foster innovation and practical learning among school students, promoting interest in electronics and programming from an early stage.

The collaboration also included an interactive training session for school faculty and students, introducing them to the basics of Arduino programming and applications. By bridging the gap between school-level science education and real-world technology, this initiative not only enhances technical exposure but also encourages students to engage in creative problem-solving and project-based learning.



ALUMNI MEET

26 January 2024

The Alumni Association Meeting of Ahalia School of Engineering and Technology was held on 26th January 2024 at the college campus. The event aimed to strengthen the bond between the institution and its alumni, while also discussing future initiatives and collaborations.

During the meeting, new office bearers were elected to lead the association and drive alumni engagement activities forward. The meeting witnessed active participation from alumni across various departments, including several from the Electronics and Communication Engineering (ECE) department.

The gathering served as a valuable platform for networking, sharing experiences, and exploring ways alumni can contribute to the growth and development of the institution.



IEEE STUDENTS BRANCH OF ASET CONDUCTS EXECUTIVE COMMITTEE MEETING

8th February 2024

On 8th February 2024, the IEEE Student Branch of Ahalia School of Engineering and Technology conducted an Executive Committee meeting with the objective of planning future activities and enhancing organizational effectiveness. The meeting brought together the branch's executive members to discuss upcoming technical events, student engagement strategies, and collaboration opportunities.

Key topics included the scheduling of workshops, seminars, and outreach programs aimed at increasing student participation and professional development. The committee also emphasized the importance of building industry connections and fostering a strong professional network through IEEE initiatives.

The meeting concluded with a renewed commitment from all members to strengthen the branch's presence within the institution and the larger IEEE community, setting a positive tone for the upcoming term.



IEEE STUDENT BRANCH MEETING

10th February 2024

On February 10, 2024, IEEE LINK organized a Face to Face (F2F) program at Hotel Dimora, Thiruvananthapuram, aimed at equipping Student Branch leaders with essential knowledge, tools, and leadership strategies.

The event witnessed active participation from Student Branch Chairs and ExeCom members across Kerala, alongside the IEEE Kerala Section Team and the LINK Team 2024. Key sessions covered topics such as IEEE hierarchy, ExeCom coordination, vTools usage, funding procedures, and the benefits of IEEE membership.

Notably, students from the Electronics and Communication Engineering (ECE) department of Ahalia School of Engineering and Technology were part of this enriching program, gaining valuable insights and networking with peers from across the state.



One-Day Workshop Session on Hackathon

4th March 2024

To foster innovation, creativity, and real-world problem-solving among students, the IEEE and IEEE Women in Engineering (WIE) ASET organized a One-Day Hackathon Workshop Session on March 4, 2024, at Visvesvaraya Hall, ASET. The event aimed to demystify the concept of hackathons and prepare students to confidently participate in such competitive and collaborative environments.

The workshop was inaugurated by Dr. Krishna Kumar Kishor, Vice Principal, ASET, and featured Ms. Sherhin P P—2nd runner-up of Chakravyuha, a 24-hour National Level Hackathon—as the Guest of Honor and session facilitator.

The session enabled students to gain practical insights into how hackathons function and how they can effectively contribute as team members and innovators.

In the afternoon, a mini Hackathon was conducted where students formed teams and brainstormed ideas to solve real-world challenges within a limited time. This hands-on experience offered a dynamic platform to apply the concepts learned in the morning session, enhancing their understanding of rapid innovation, teamwork, and solution-driven thinking.

The event was a resounding success, offering ECE students a valuable opportunity to explore the intersection of technology and creativity. It also highlighted the department's ongoing commitment to promoting experiential learning and preparing students for industry-relevant challenges.

The ECE Department congratulates IEEE WIE ASET for organizing this impactful event and commends all participants for their active engagement and enthusiasm.



“Tink-Her-Hack”

Empowering Innovation Through Women-Only Hackathon

8th & 9th March 2024

The IEEE Student Branch of ASET, in association with the ECE Department and IEEE Women in Engineering (WIE), and in collaboration with the Tinker Hub Foundation, proudly organized “Tink-Her-Hack”, a premier women-only hackathon. Held on March 9th and 10th, this groundbreaking event marked a major step in promoting diversity, inclusion, and innovation in the tech community.

Bringing together over 1000 talented women from across Kerala, the event was conducted across multiple venues, creating a vibrant space for collaboration, creativity, and technical excellence. Participants engaged in coding challenges, creative problem-solving, and idea development, showcasing their skills and pushing the boundaries of innovation.

Supported by mentors and a strong peer network, the hackathon fostered an empowering environment where participants could freely explore, learn, and grow. The involvement of the ECE Department added academic and technical value to the initiative, reinforcing its goal of bridging education with practical, real-world problem-solving.

“Tink-Her-Hack” successfully celebrated the spirit of women in technology and highlighted the importance of inclusive platforms in shaping the future of STEM. It was not just a hackathon—it was a powerful movement that brought together passion, purpose, and progress.

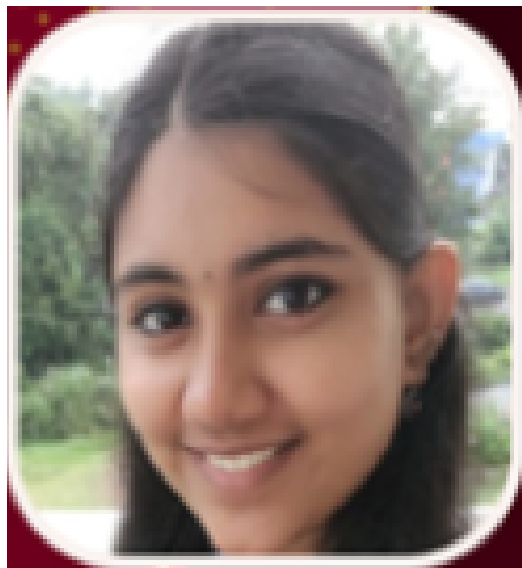


2023-2027 KTU S1

Academic Topper

The Department of Electronics and Communication Engineering proudly congratulates Ms. Namitha S Kumar for securing the highest SGPA in the S1 KTU examinations for the 2023–2027 batch.

Her exceptional academic performance in the very first semester reflects her dedication, discipline, and strong foundation in engineering studies. As she continues her academic journey, the department extends best wishes for continued success and excellence in the semesters ahead.

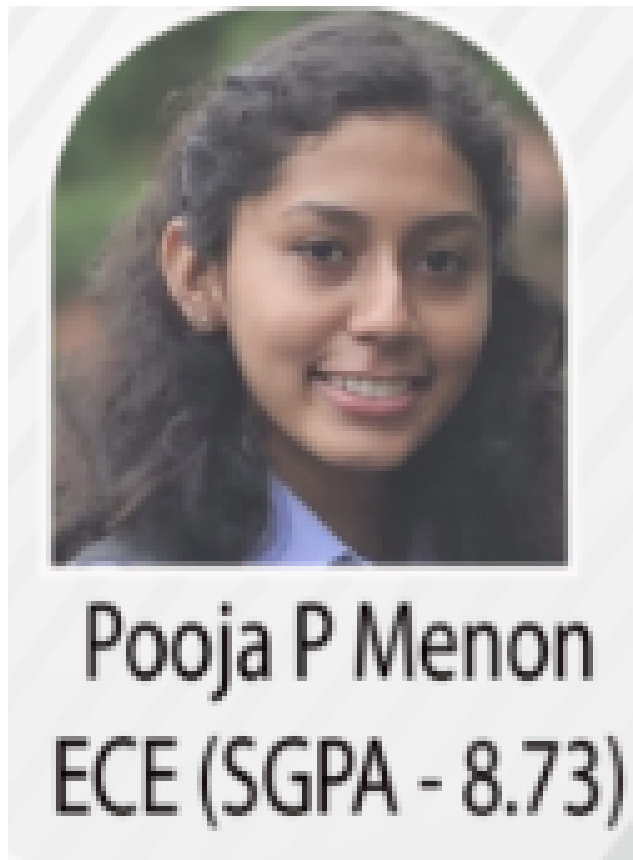


We celebrate and support our students' achievements as a reflection of the academic strength and spirit of the ECE Department.

2022-2026 KTU S3

Academic Topper

The Department of Electronics and Communication Engineering is proud to recognize and celebrate the academic achievements of our students. In the S3 KTU examinations (2022–2026 batch), Ms. Pooja P Menon has emerged as the academic topper of the department with an impressive SGPA of 8.73. Her consistent dedication, commitment to academic excellence, and perseverance have set a remarkable example for her peers. The department congratulates Ms. Pooja on this outstanding achievement and wishes her continued success in the semesters ahead.



We take pride in our students' accomplishments and remain committed to nurturing academic excellence and holistic development.

2021-2025 KTU S5

Academic Topper

The Department of Electronics and Communication Engineering is pleased to acknowledge the academic accomplishment of Ms. Megha P, who has secured the highest SGPA of 7.93 in the S5 KTU examinations for the 2021–2025 batch.

Her achievement reflects her hard work, focused effort, and determination to excel academically. The department extends heartfelt congratulations to Ms. Megha and encourages her to continue striving for excellence in the upcoming semesters.

We remain committed to fostering a culture of academic rigor and student success.



2020-24 KTU S7

Academic Topper

The Department of Electronics and Communication Engineering is delighted to announce Ms. Sreelakshmi V N as the academic topper of the S7 KTU examinations for the 2020–2024 batch, with an outstanding SGPA of 8.87. Her remarkable performance is a testament to her hard work, consistency, and academic commitment throughout her engineering journey.



The department congratulates Ms. Sreelakshmi on this well-deserved achievement and wishes her continued success in all future endeavors.

The ECE Department takes immense pride in the accomplishments of its students and remains dedicated to nurturing academic and professional excellence.

ECE Students Placed through QSpiders Campus Recruitment Drive

The Department of Electronics and Communication Engineering is proud to share the placement success of its students in the QSpiders Campus Recruitment Drive. QSpiders, a renowned software training and placement organization, provided a valuable platform for aspiring graduates to begin their professional journey in the tech industry.

We are delighted to announce that Ms. Nithyasree, Ms. Sneha Krishna, Ms. Amritha M H, and Ms. Sreelakshmy V N, final-year students from the ECE Department, have been successfully selected through this recruitment drive. Their achievement highlights not only their individual talent and dedication but also the department's commitment to preparing students for industry readiness.

The department congratulates the selected students on this milestone and wishes them continued growth and success in their careers. Their accomplishment stands as an inspiration for their peers and juniors.



Machine Learning in VLSI Design and Verification

A Smarter Path to Chip Innovation

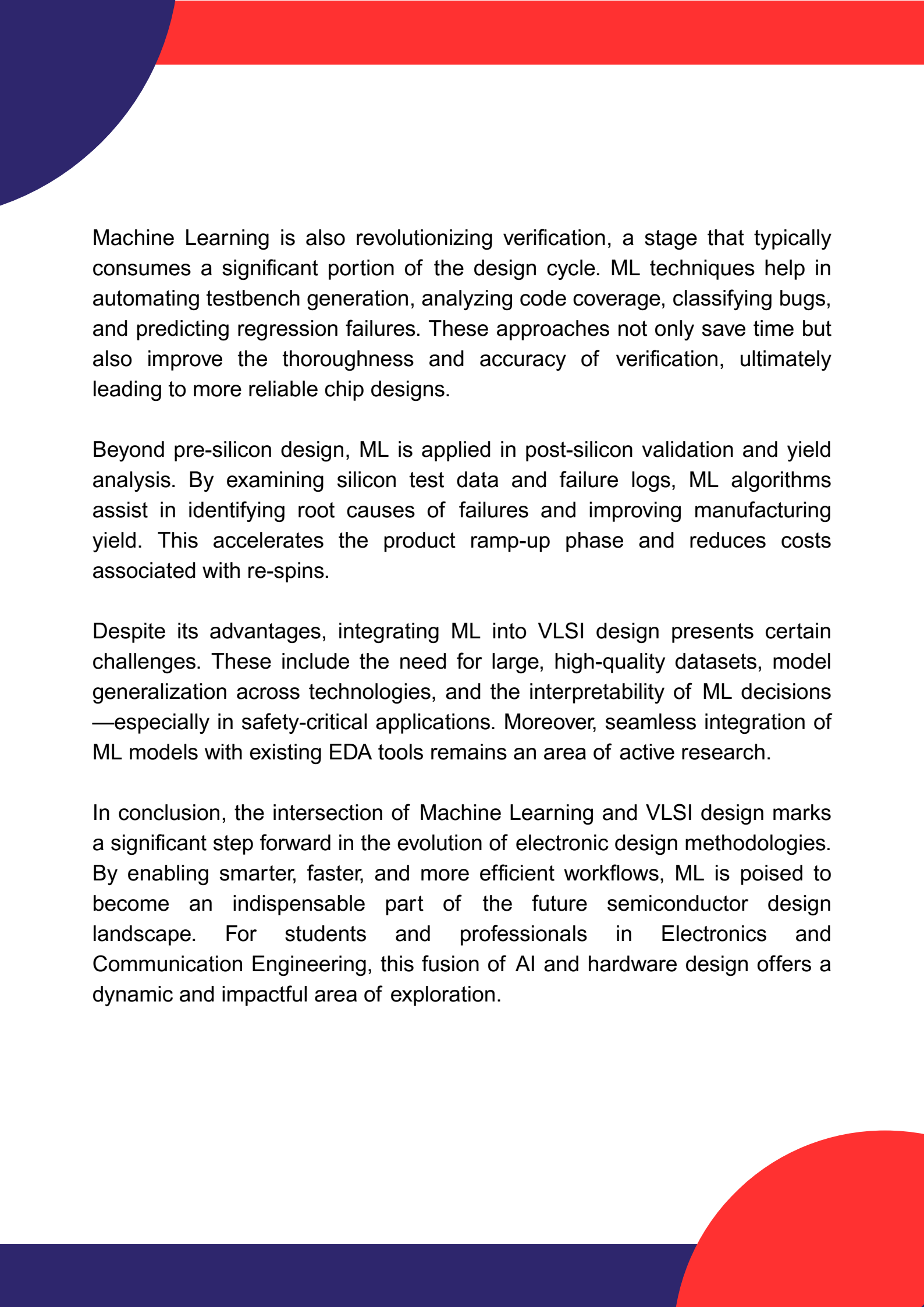


Dr V Balamurugan

With the ever-increasing demand for high-performance, low-power, and compact integrated circuits, the complexity of VLSI (Very-Large-Scale Integration) design has grown substantially. Traditional Electronic Design Automation (EDA) tools, though effective, face limitations in handling the scale and intricacy of modern chip designs. To address this, Machine Learning (ML) techniques are being increasingly adopted to enhance and accelerate various stages of the VLSI design and verification process.

Machine Learning, which involves training algorithms to identify patterns and make predictions from data, finds extensive applicability throughout the VLSI design flow. The design process—from RTL (Register Transfer Level) design to synthesis, placement and routing, and physical verification—generates vast amounts of data. ML models can analyze this data to optimize design parameters, predict timing and power metrics, and assist in intelligent decision-making, thereby improving design efficiency and quality.

In the design phase, ML is particularly effective in design space exploration. By predicting outcomes such as timing delays or power consumption for different architectural configurations, ML models reduce the need for exhaustive simulations. In physical design, algorithms such as supervised learning and reinforcement learning are used to optimize cell placement and routing, minimizing congestion and improving timing closure. These data-driven methods have shown to outperform heuristic-based approaches in certain scenarios.



Machine Learning is also revolutionizing verification, a stage that typically consumes a significant portion of the design cycle. ML techniques help in automating testbench generation, analyzing code coverage, classifying bugs, and predicting regression failures. These approaches not only save time but also improve the thoroughness and accuracy of verification, ultimately leading to more reliable chip designs.

Beyond pre-silicon design, ML is applied in post-silicon validation and yield analysis. By examining silicon test data and failure logs, ML algorithms assist in identifying root causes of failures and improving manufacturing yield. This accelerates the product ramp-up phase and reduces costs associated with re-spins.

Despite its advantages, integrating ML into VLSI design presents certain challenges. These include the need for large, high-quality datasets, model generalization across technologies, and the interpretability of ML decisions—especially in safety-critical applications. Moreover, seamless integration of ML models with existing EDA tools remains an area of active research.

In conclusion, the intersection of Machine Learning and VLSI design marks a significant step forward in the evolution of electronic design methodologies. By enabling smarter, faster, and more efficient workflows, ML is poised to become an indispensable part of the future semiconductor design landscape. For students and professionals in Electronics and Communication Engineering, this fusion of AI and hardware design offers a dynamic and impactful area of exploration.

WORD POWER



Mr Abhijith V

What does it mean when someone is Lurking? To Lurk means to hide for some evil or unethical purpose. For example, The customs officials found the smugglers lurking around the bushes. The entire movie of “Garudan” lurks around a rapist who is brought to light by a policeman.

The amount of aid received by the Government for the flood affected people is only going to scratch the surface. Scratch the surface? If you scratch the surface of an issue you are just covering a very small portion of the issue and not going to solve it. By just knowing how to answer the previous year question papers, the students hardly scratch the surface of the subject.

Something that could not be depended upon is generally characterized by the word Precarious. Precarious denotes instability and insecurity. The origin of the word is from Latin *precis*, or from the old Latin, *praecidere*. People living down the mountains lead a precarious life. They are subject to animal attacks anytime.

Another word imprecate, means to pray against something or someone. For instance, People’s imprecations led to the failure of the ruling party.

Blitzkrieg is a Germanic word meaning lightning war. A sudden strike conducted with great speed and force. Nature unleashed a blitzkrieg when the streets were flooded by the cloudburst. Blitzkrieg is used commonly to denote an unexpected strike or blow. The word has its origins in the Second World War. It was Nazi military tactic to destroy their enemies by the psychological shock created by a surprise attack with speed and vigor.

Wrap your head around something is an idiom used to denote to adapt or learn or accept something that is difficult to understand. Students had a tough time wrapping their head around the concept that was taught today in the class. The logic behind the solution to the problem discussed had wrapped their head for some time. What happens when you enter a Hornet's nest? The speaker stirred up the hornet's nest when he spoke about the inability of the students. The referee in the soccer tournament entered into the hornet's nest when he justified his error in judgment. The leader of the ruling party was expecting to enter the hornet's nest when he reached the accident spot. Entering into a hornet's nest means entering into a dangerous situation. Suspension of the MP's in the parliament by the Vice-President stirred up the hornet's nest.

What does Stultify mean? Stultifying something means to prevent something from development or improvement or negate an idea or thought. The opposition stultified the Government's new law. The industrial visits planned by the students.

APJAKTU E-Zone Football Championship 2023-24

The Football Team of Ahalia School of Engineering and Technology (ASET) actively participated in the APJ Abdul Kalam Technological University (APJAKTU) E-Zone Football Championship, held at NSS College of Engineering (NSSCE), Palakkad.

The tournament witnessed spirited participation from various colleges under the E-Zone. The ASET team displayed commendable teamwork, determination, and sportsmanship throughout the matches. Notably, several students from the Electronics and Communication Engineering (ECE) department were part of the team, contributing significantly to the team's performance.

The event provided a valuable platform for students to showcase their athletic talents and build camaraderie through sports



MANU S VIGNESH WINS SILVER MEDAL IN MR. KERALA (JUNIOR) BODYBUILDING CHAMPIONSHIP 2023-24



The Ahalia School of Engineering and Technology (ASET) is proud to announce that Manu S Vignesh, a student of S4 ECE, has secured the Silver Medal in the Mr. Kerala (Junior) Bodybuilding Championship 2023–24.

This remarkable achievement highlights Manu's dedication, discipline, and hard work in the field of bodybuilding, while also bringing pride and recognition to the institution. The ASET community congratulates Manu on this commendable success and wishes him continued excellence in both academic and athletic pursuits.

ASET Cricket Team Shines in APJAKTU E-Zone Championship

The ASET Cricket Team proudly represented the institution in the APJAKTU E-Zone Cricket Championship, held at Government Engineering College, Thrissur, on 12th February 2024. The team displayed commendable sportsmanship, discipline, and determination throughout the tournament. Comprising talented and enthusiastic players from various departments, including ECE, the team showcased their unity and competitive spirit on the field. Guided by their coach and supported by the sports coordinators, the players trained rigorously in the weeks leading up to the event. Their performance not only highlighted their individual skills but also reflected the collaborative effort that defines team sports.

Participation in such prestigious tournaments reinforces the importance of extracurricular engagement in holistic student development. The ECE Department congratulates the team for their dedication and applauds their representation of ASET on a university-level platform.



ASET Soccer League – Season 5

ECE Students Shine in Campus Football Fever

The ASET Soccer League – Season 5 (ASL 5) was successfully conducted from 21st February 2024 at the ASET Football Ground, with enthusiastic participation from students across various departments. Several students from the ECE Department actively took part in the tournament, showcasing great teamwork, sportsmanship, and energy both on and off the field.



ECE Students Shine at APJAKTU Interzonal Tug of War Championship

The APJAKTU Interzonal Tug of War Championship, held on March 6, 2024, at KMEA, Aluva, witnessed the spirited participation of both the Men's and Women's Tug of War teams of ASET. This prestigious event brought together top teams from various zones, showcasing strength, strategy, and team spirit.

Representing ASET with pride and vigor, the teams delivered commendable performances, displaying exceptional coordination and determination on the field. Notably, students from the ECE Department played an active and dynamic role in both the men's and women's squads. Their enthusiasm, commitment to training, and teamwork significantly contributed to the overall performance and morale of the teams.

The participation in such intercollegiate events highlights the department's encouragement of holistic student development beyond academics. The involvement of ECE students in the championship stands as a testament to their dedication, discipline, and sportsmanship.

Participation in sports plays a vital role in shaping the all-round development of students. It instills qualities such as discipline, leadership, perseverance, and teamwork—skills that are equally valuable in academic and professional life. Encouraging involvement in sporting activities not only promotes physical well-being but also helps students manage stress and build a positive outlook. The ECE Department firmly supports such opportunities that foster a healthy balance between academics and co-curricular excellence.

The department congratulates all participants for their efforts and wishes them continued success in future tournaments and endeavors !



ECE Students Represent ASET at 6th APJAKTU Interzonal Athletics Championship

The ASET Athletics Team 2024 proudly participated in the 6th APJAKTU Interzonal Athletics Championship, held at the iconic Chandrashekharan Nair Stadium, Thiruvananthapuram. This prestigious event, featuring top athletes from various engineering institutions across Kerala, served as a platform to showcase talent, determination, and sportsmanship at a grand level.

The team competed with great enthusiasm and dedication across various track and field events, striving for excellence and bringing pride to the institution. Notably, students from the ECE Department were active contributors to the ASET Athletics Team, participating in multiple events and demonstrating outstanding athletic spirit and commitment.

Their involvement underscores the holistic development promoted by the department—where academic excellence goes hand-in-hand with physical fitness, discipline, and personal growth. Participation in such high-level competitions encourages students to push their limits, develop resilience, and foster a spirit of healthy competition.



The ECE Department congratulates all student-athletes for their commendable efforts and applauds their role in representing ASET at the interzonal level with pride and perseverance.

ASET Premier League Season 5

Celebrating Cricketing Spirit

The Cricket Club of ASET successfully conducted the much-anticipated ASET Premier League (APL) Season 5 from 1st April to 8th May 2024 at the ASET Sports Ground. The tournament brought together students and faculty with a shared passion for cricket, promoting sportsmanship, teamwork, and competitive spirit across the campus.

This year's APL witnessed the participation of six energetic teams, with matches coordinated by Mr. Bibin B, Assistant Professor in Physical Education. The tournament began with a round-robin format, where Team Rising Rockers emerged as league toppers, securing 8 points. The semifinal and final matches followed a knockout format with eliminator rounds, adding intensity and excitement to the competition.

The grand finale was held on 8th May 2024, and was inaugurated by Dr. P. R. Sreemahadevan Pillai, Principal, ASET, who also graced the event as a special guest and witnessed the thrilling match. Team Professors XI emerged as the champions, while Team Rising Rockers secured the runner-up position. The Principal awarded winner's trophies and individual medals, acknowledging the commendable efforts of all participants.

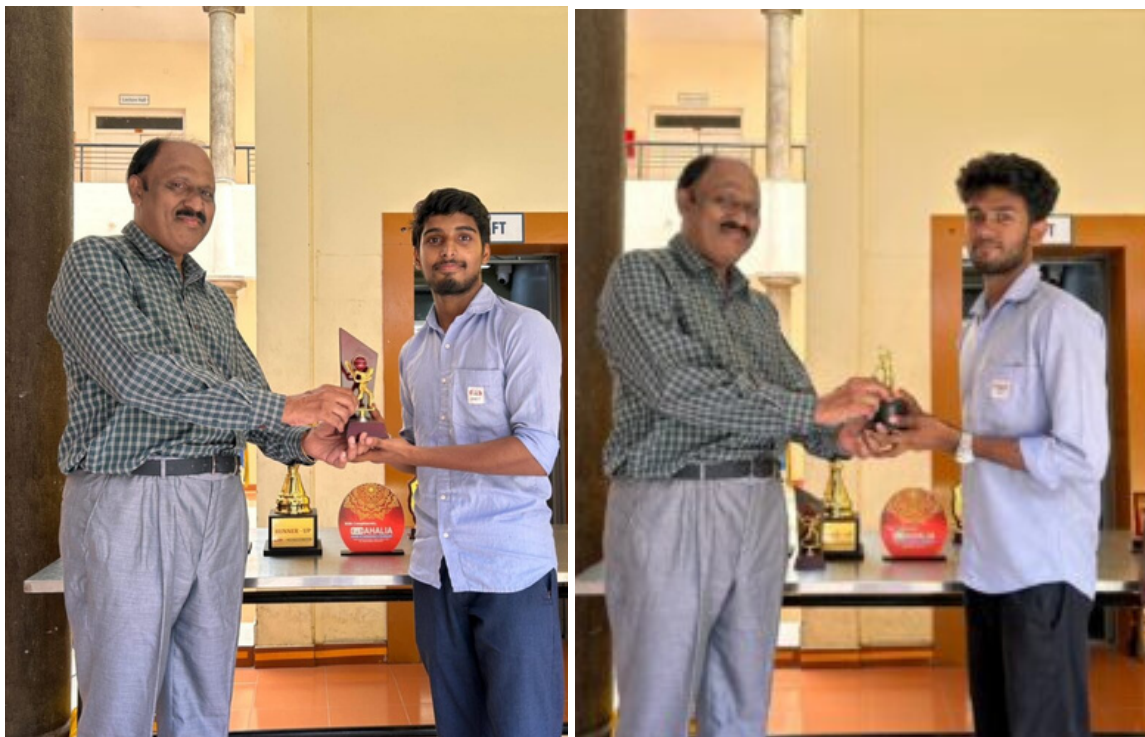


The Department of Electronics and Communication Engineering takes immense pride in the performance of its students:

- Anirudh B (Rising Rockers) was awarded the prestigious title of Player of the Tournament for his all-round performance.
- Arun (Rising Rockers) was named Best Bowler of the Tournament, securing the highest number of wickets.

With over 90 students participating, APL Season 5 stood out as a vibrant and enthusiastic celebration of sports, unity, and talent. The event not only encouraged physical fitness but also fostered strong interdepartmental camaraderie and spirit.

The ECE Department congratulates all participants, especially our star performers, and looks forward to more such energetic and engaging events in the future.





AHALIA SCHOOL OF ENGINEERING & TECHNOLOGY

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