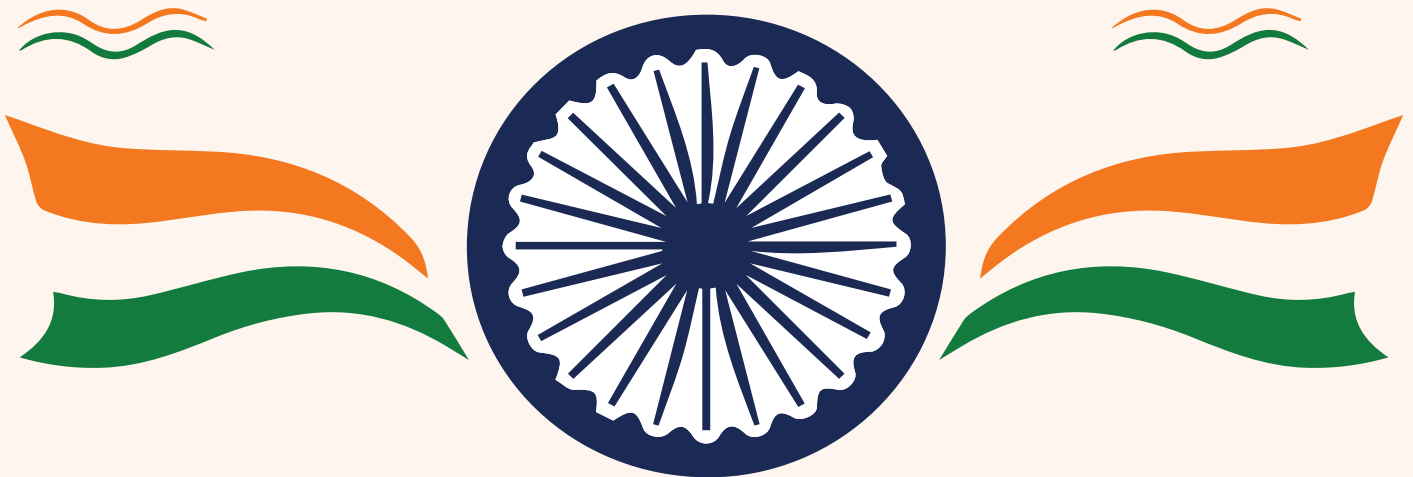


ASET NEWS

VOL 11. ISSUE 08
AUGUST 2026

A Monthly newsletter of
**Ahalia School of Engineering
& Technology**

HAPPY INDEPENDENCE DAY



VISION

Grow as a center of learning and research, transforming students to professionals with knowledge, skill, competence, commitment, confidence through decisive learning and contribute to the sustainable development of the society.

MISSION

1. To instil technical expertise in order to address current and emerging challenges in the quest for creating sustainable and high-quality livelihoods.
2. To foster a culture of research, innovation, and entrepreneurship through determined learning.
3. To promote an environment that supports the welfare of society through ethical and professional conduct.

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PRINCIPAL MESSAGE

Importance of Wind Energy In India

Dr. P.R. Suresh
Principal

India stands at a critical juncture where rapid economic growth intersects with the urgent need for environmental sustainability. As the world's most populous nation, India's energy demand is escalating at an unprecedented rate. To power its expanding industries and cities without exacerbating climate change, the nation must aggressively transition away from fossil fuels. Among the various renewable options, wind energy has emerged as a cornerstone of India's clean energy strategy, offering profound environmental, economic, and strategic advantages.

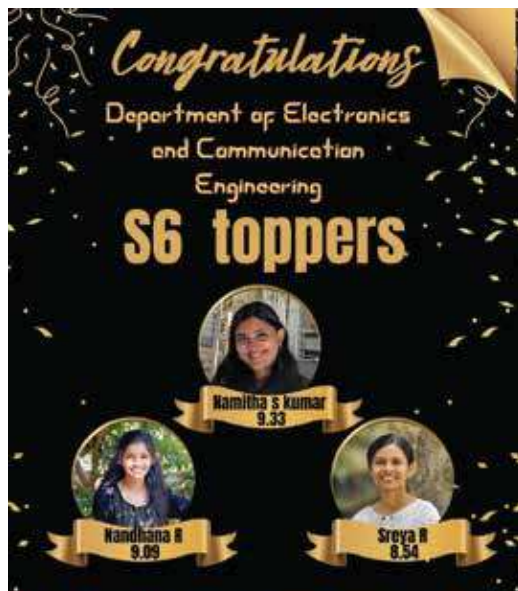
From an environmental perspective, wind energy is indispensable for India's climate commitments. India has set an ambitious target to achieve net-zero carbon emissions by 2070 and to install 500 gigawatts of non-fossil fuel energy capacity by 2030. Utilizing wind power directly mitigates greenhouse gas emissions, combats severe urban air pollution, and conserves massive amounts of water that would otherwise be consumed by thermal power plants.

Economically, wind energy drives domestic growth and energy security. India possesses a vast coastline of over 7,500 kilometres and expansive windy plains, particularly across states like Tamil Nadu, Gujarat, Maharashtra, and Rajasthan. Capitalizing on these natural resources reduces India's heavy reliance on expensive, volatile fossil fuel imports, thereby stabilizing the national economy. Furthermore, the wind sector is a robust catalyst for job creation, offering employment in manufacturing, project installation, engineering, and maintenance, while fostering a booming domestic supply chain.

Technologically, India is advancing from traditional onshore installations to pioneering offshore wind projects. Offshore wind opens massive, untapped generation potential with stronger and more consistent wind speeds. When integrated with solar power into hybrid systems, wind energy helps solve the issue of intermittent supply, as wind speeds often peak during the evening and night when solar generation drops. India aims to achieve **100 GW** of cumulative wind energy capacity by **2030** and scale up to **156 GW** by **2036**, backed by over **56.1 GW** of currently installed capacity.

In conclusion, wind energy is not merely an alternative source of electricity for India; it is a strategic necessity. By harnessing its immense wind potential, India can successfully decouple its economic expansion from environmental degradation, ensuring a secure, self-reliant, and sustainable future for generations to come.

ACHIEVEMENTS



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DEPARTMENT OF
ARTIFICIAL INTELLIGENCE & MACHINE LEARNING

S4 - TOPPERS
(BATCH 2024-26)

ANJANA PRAKASH
SGPA: 9.2

AKSHARAA D
SGPA: 9.2

HIBA FATHIMA M
SGPA: 9.2

SREYA S
SGPA: 9.2

ANASIMKA S
SGPA: 9.2

Congratulations

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CONGRATULATIONS TO OUR S4 TOPPERS

1ST PLACE
ANJALI B
SGPA: 8.46

2ND PLACE
NISHAD A
SGPA: 8.06

FROM THE
DEPARTMENT OF CIVIL ENGINEERING
2024-28 BATCH

★ Your hard work today builds the stronger tomorrow ★

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Congratulations to
Mechanical Engineering S4 Toppers
(Batch 2024-2028)

Ujjwal Jagadeesh
Deneesh P R
Arjun R

Department of Mechanical Engineering
Ahalia School of Engineering & Technology
Proud of Your Achievement!

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CONGRATULATIONS! TO OUR S4 TOPPERS

1ST PLACE
ADITHYAN MARAYAN V
SGPA: 9.38

2ND PLACE
ATHULYA K U
SGPA: 9.38

3RD PLACE
KABANDITH K J
SGPA: 9.38

FROM THE
DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING
2024 - 2028 BATCH CSE (A)

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Department of Computer Science and Engineering
SA UNIVERSITY EXAM TOPPERS
CSE-B

CONGRATULATIONS

1ST PLACE
SNEHA S
SGPA: 9.0

2ND PLACE
RASHMI B
SGPA: 9.0

3RD PLACE
NATANEETH S
SGPA: 9.0

4TH PLACE
SANTIYA S
SGPA: 9.0

5TH PLACE
LENI D
SGPA: 9.0

6TH PLACE
HEERA R
SGPA: 9.0

7TH PLACE
SHRIMA BARDASAV
SGPA: 9.0

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Congratulations TO OUR S4 TOPPERS

1ST PLACE
ZOYA PRAKASAN
SGPA: 9.38

2ND PLACE
ABIYA K BAJU
SGPA: 9.29

3RD PLACE
KARTHIKA KRISHNA
SGPA: 8.25

DEPARTMENT OF ELECTRONICS AND COMMUNICATION
2024-28 BATCH

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Department of Artificial Intelligence & Machine Learning
Congratulations
Toppers of the B.Tech S2 (2025-2029 Batch)



ARYA A



ATHULYA M



ABHISHEK B

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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING
S2 CSE B Toppers



U Sreelakshmi
8.89



Reshmi K
8.59



Krishnakripa K
8.28

Congratulations!!!

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S2 TOPPERS
2025-2029 BATCH



1 NIVEDIA G GUPTA



2 PADHYA DAS



3 ABHISHEK N K



3 RASEENA K

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING
CONGRATULATIONS

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Department of Mechanical Engineering
Congratulations
Toppers of the B.Tech S2 (2025-29 Batch)



SIDHARTH N R
First



ARYA R
Second



FARHAN A
Third



AASRITHA U
Fourth

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CONGRATULATIONS TO OUR S2 TOPPERS
DEPARTMENT OF CIVIL ENGINEERING
2025-29 BATCH



1ST ABHINAV P
1st Topper



2ND ISA I
2nd Topper



3RD NETHRA PROMOD
3rd Topper

We are proud of you!
KEEP SHINING AND INSPIRING!

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Department of Computer Science and Engineering
S2 CSE A



AISWARYA A
(8.74)



ADITHYA ARUN KRISHNAN
(8.52)



G SREELAKSHMI
(8.3)



ASHLYN SANTHOSH
(8.13)

Congratulations TO THE WINNERS

DEPARTMENT OF CIVIL ENGINEERING

INTERNSHIP AT



Congratulations!



R ADITH



ATHUL M



SANDHVARNA P



SREE
NANDANA P S



PARVATHY S



SABNA T M



NANDANA K C

Your hard work and dedication
have truly paid off.

B.Tech 2023-27 Batch

Wishing you a rewarding learning experience
and continued success ahead!

Congratulations!

WE ARE PROUD TO ANNOUNCE THAT
AHALIA SCHOOL OF ENGINEERING & TECHNOLOGY
HAS BEEN RECOGNIZED AS AN
ACTIVE LOCAL CHAPTER
UNDER THE PRESTIGIOUS
NPTEL-SWAYAM INITIATIVE

ASPIRANT
JAN-APR
2026

IEEE SB ASET
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Shreeam V
VICE CHAIRPERSON

Nandana L
JOINT SECRETARY

Zoya Prakashan
TREASURER

Nethra S
TREASURER

Anoop M
MEMBER

Congratulations!
ON YOUR
PLACEMENT

We are proud of your achievement and wish you a
successful and fulfilling career ahead!

SULFATH H
placed at
MTA
ENGINEERING & CONSTRUCTION
PALAZZO, KADAPPAZHA
www.mta-india.com

AKASH B
placed at
TRITECH
TECHNOLOGIES

VIDHUN N
placed at
ULTRA
RESEARCH ENGINEERING

From
DEPARTMENT OF CIVIL ENGINEERING
B.TECH 2022-26 BATCH

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ASET ACTIVITIES

Faculty Development Programme – EDU AI : Redefining the Art of Teaching

The Department of Mechanical Engineering & Computer Science Engineering successfully organized a Six-Day Faculty Development Programme (FDP) titled “EDU AI: Redefining the Art of Teaching” with the objective of empowering educators to effectively integrate Artificial Intelligence into teaching, learning, research, and academic administration. The programme served as a platform for faculty members to explore the transformative potential of AI technologies in higher education and to develop practical skills that enhance instructional effectiveness and research productivity.

The FDP featured a balanced combination of expert lectures, interactive discussions, demonstrations, and hands-on training sessions. Participants were introduced to the fundamentals of Artificial Intelligence, Generative AI, Large Language Models (LLMs), prompt engineering, open-source AI platforms, AI-assisted research tools, and emerging trends in AI-enabled education. The practical sessions provided participants with opportunities to work directly with AI tools for creating lesson plans, lecture notes, assessments, presentations, research content, and other teaching resources.

A major highlight of the programme was its emphasis on experiential learning. Faculty members gained hands-on experience in designing effective prompts, deploying local AI models, conducting AI-assisted literature reviews, creating digital teaching materials, and developing complete AI-supported teaching workflows. These activities enabled participants to understand the practical applications of AI while promoting ethical and responsible use of the technology in academic environments.

Objectives

The major objectives of the FDP were to:

- Familiarize faculty members with recent developments in Artificial Intelligence and Generative AI.
- Introduce AI tools that support teaching, learning, research, and academic administration.
- Develop effective prompt engineering skills for educational applications.
- Provide practical exposure to open-source and locally deployable AI models.
- Enhance research productivity using AI-assisted literature review and content development tools.
- Encourage innovative, technology-enabled teaching practices.

Outcomes

The programme significantly enhanced the participants’ understanding of AI technologies and their applications in higher education. Faculty members acquired practical skills in utilizing AI tools for curriculum planning, instructional material preparation, assessment design, academic writing, presentation development, and research support. The FDP also strengthened participants’ confidence in adopting AI-enabled teaching methodologies while fostering innovation, collaboration, and continuous professional development.

Feedback

The programme received an excellent response from the participants. The interactive sessions, practical demonstrations, and hands-on workshops were highly appreciated for their relevance and applicability. Participants expressed that the knowledge and skills gained during the FDP would greatly support their teaching, research, and academic responsibilities.

Conclusion

The Five-Day Faculty Development Programme on “EDU AI: Redefining the Art of Teaching” concluded successfully with enthusiastic participation from faculty members. The programme effectively met its objectives by providing comprehensive exposure to Artificial Intelligence and its educational applications. It served as an excellent platform for enhancing digital competencies, promoting innovative teaching practices, and preparing educators to leverage AI technologies responsibly in higher education. The successful completion of the programme reflects the institution's commitment to fostering academic excellence, innovation, and continuous professional development among its faculty members.



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5 DAY
FACULTY DEVELOPMENT PROGRAM

EduAI:

Redefining the Art of Teaching

22 June 2026 - 27 June 2026

5 Days of Learning . Sharing . Innovating

ORGANIZED BY

Organized by Department of Mechanical Engineering & Computer Science and Engineering

IN COLLABORATION WITH



ZENDALONA
VISION BEYOND EIGHT

VENUE
Tim Berners Lee Lab (Lab4)





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UPI ID:
116543816007002@canb

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Mukundan Annamalai
Product Head

Nalin Sathyan
Product head

Nihal Rode
(100c Winner 2 times)
Technical consultant

COORDINATORS

Mr. Dhanesh S
Assistant Professor
Mechanical Engineering

Ms. Deepthy PS
Assistant Professor
Computer Science and Engineering







അധ്യാപക പരിശീലനം

പാലക്കാട്: അപരിചിതമായ ഒരു പുതിയ ലോകത്തിൽ ആദ്യം കൈമാറ്റം ചെയ്ത, സെൻ്റലായോനയ്ക്ക്, മേർന്നു സംഘടിപ്പിക്കുന്ന അഞ്ചുദിവസത്തെ ഫോക്കസ് ഡിവിഷൻ മേന്മ പ്രോഗ്രാം: 'എഡു എ.ഐ.' ന് ഡിവിഷൻ മേന്മയ്ക്ക് ഓൺലൈൻ 22-ന് വിശ്വസ്തമായ ഹാളിൽ ആരംഭിച്ചു. അധ്യാപനത്തിലും ഗവേഷണത്തിലും നിർമ്മാണത്തിലും പ്രായോഗികസാധ്യതകൾ പരിചയപ്പെടുത്തുക എന്ന ലക്ഷ്യത്തോടെ സംഘടിപ്പിച്ച പരിപാടി പ്രിയപ്പെട്ട സോ. പി.ആർ. സുരേഷ് ഉദ്ഘാടനം ചെയ്തു. നിശ്ചിത ഡിജിറ്റൽ സോ. പി.ആർ. മൂലകാനന്ദ്, പ്രൊഫ. ഓഫ് മ്യൂസിക് അക്കാദമിയിൽ എൻ.വി. സംസാരിച്ചു.

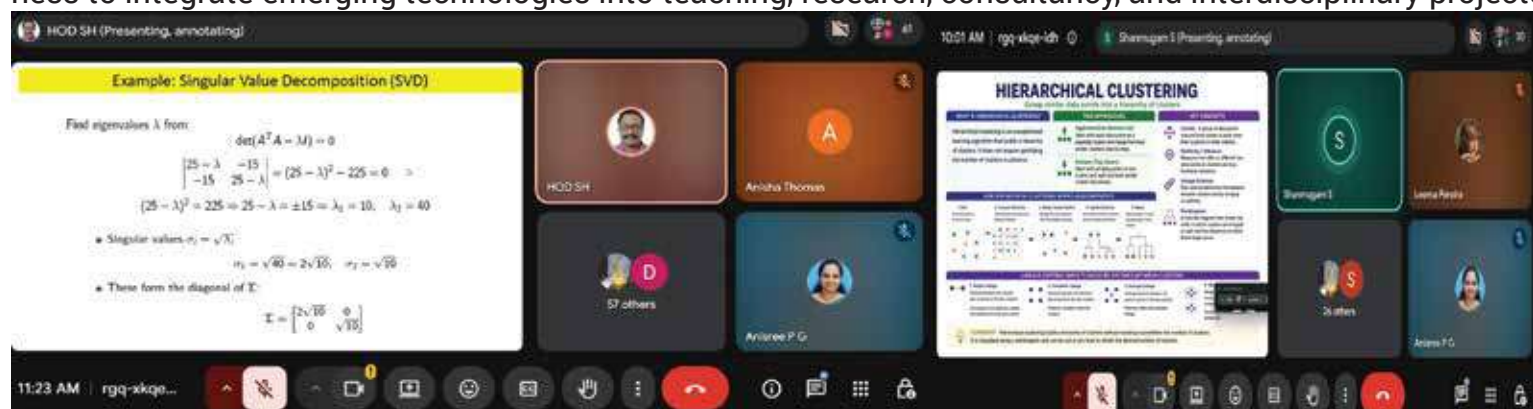
പരിശീലനപരിപാടിയിൽ എ.ഐ. അധിഷ്ഠിത അധ്യാപനത്തിൽ കരം, ഗവേഷണ ഉപകരണങ്ങൾ, അക്കാദമിക് ഉദ്യോഗസ്ഥരുടെ വർദ്ധിപ്പിക്കുന്ന സാങ്കേതികവിദ്യകൾ എന്നിവയിൽ വിദഗ്ദ്ധർ ക്ലാസ്സുകളും, പ്രായോഗികപരിശീലനവും നൽകും.

24/06/2026 PALAKKAD Pg 02



Faculty Development Programme on “Machine Learning: Algorithms, Applications & Insights”

The Department of Computer Science and Engineering, Ahalia School of Engineering and Technology, successfully organized a six-day Faculty Development Programme (FDP) on “Machine Learning: Algorithms, Applications & Insights.” The programme was conducted under the convenorship of Dr. S. Gunasekaran, Professor and Head, Department of Computer Science and Engineering, with Ms. Anisree P. G., Assistant Professor, Department of Computer Science and Engineering, serving as the Programme Coordinator. The primary objective of the FDP was to strengthen the knowledge and technical competencies of faculty members in the rapidly evolving field of Machine Learning. The programme was designed to provide participants with a comprehensive understanding of the mathematical foundations, machine learning algorithms, model development techniques, evaluation methodologies, and recent advancements in artificial intelligence. Equal emphasis was placed on theoretical concepts and practical implementation to enhance teaching effectiveness and research capabilities. The programme featured expert sessions delivered by distinguished academicians and industry professionals from reputed institutions and organizations. The sessions covered a wide range of topics, including the mathematical foundations of machine learning, supervised and unsupervised learning techniques, regression and classification algorithms, optimization methods, support vector machines, neural networks, dimensionality reduction, clustering techniques, ensemble learning, model evaluation metrics, and machine learning pipelines. Practical demonstrations and hands-on activities using Python, NumPy, Scikit-learn, and Keras enabled participants to gain valuable experience in implementing machine learning models on real-world datasets. Throughout the programme, the participants actively engaged in discussions, practical exercises, and interactive learning activities. The resource persons shared valuable insights into current trends, best practices, and research opportunities in Machine Learning, encouraging participants to adopt innovative teaching methodologies and apply machine learning concepts in academic and industrial problem-solving. The Faculty Development Programme successfully achieved its objectives by equipping participants with a strong conceptual foundation and practical skills in Machine Learning. The programme enhanced the participants’ understanding of modern machine learning techniques, improved their ability to develop and evaluate predictive models, and strengthened their readiness to integrate emerging technologies into teaching, research, consultancy, and interdisciplinary projects.



AICTE ATAL Academy Sponsored Faculty Development Programme (FDP)

The Department of Computer Science & Engineering, Ahalia School of Engineering & Technology, successfully organized a Six-Day ATAL Faculty Development Programme (FDP) on "Next-Generation Automated Systems: Multimodal Learning, MLOps, Responsible Data Analytics & Quantum Processing" from 20th to 25th July 2026. The programme witnessed enthusiastic participation from faculty members of AICTE-approved institutions, postgraduate students, The sessions covered emerging areas such as Trustworthy and Responsible AI, Explainable AI (XAI), AI Governance, Multimodal Learning, Transformers, Data Engineering, MLOps, Quantum Computing, and Quantum Machine Learning. Eminent academicians and industry experts led interactive technical sessions, research discussions, and hands-on learning activities, enabling participants to gain valuable insights into cutting-edge technologies and their real-world applications.



First Batch Alumni Reunion at ASET

Ahalia School of Engineering and Technology (ASET) proudly hosted a memorable Alumni Reunion for its first batch of students, bringing together former students, faculty members, and well-wishers for a heart-warming day of nostalgia, friendship, and celebration.

The reunion provided a wonderful opportunity for alumni to return to their alma mater, reconnect with old friends and teachers, and relive cherished memories of their college days. The campus was filled with joy and excitement as former students shared their personal and professional journeys and fondly recalled the experiences that shaped their years at ASET.

The gathering also served as a meaningful platform to strengthen the bond between the alumni and the institution. Faculty members expressed their happiness in welcoming back the first batch of students and appreciated their continued connection with the college. The interactions highlighted the importance of alumni engagement in contributing to the growth and development of the institution and inspiring present and future generations of students.

The enthusiastic participation of the alumni made the reunion a truly special and memorable occasion. The event concluded on a warm and positive note, with everyone expressing their desire to stay connected and contribute actively to future alumni initiatives.

The First Batch Alumni Reunion marked a significant milestone in ASET's journey, celebrating enduring friendships, shared memories, and the lasting bond between the institution and its alumni.



ASCEND 2026

Ahalia School of Engineering & Technology (ASET) celebrated ASCEND 2026, the 11th Course Completion Ceremony for the B.Tech. 2022–26 batch. The event was inaugurated with a procession, prayer, and lighting of the lamp.

The programme was presided over by Dr. P. R. Suresh, Principal, while Dr. Rajeev N., Dean Academics, delivered the welcome address. Dr. Unnikrishnan Nair, Former Director of VSSC and IIST, addressed the graduates as the Chief Guest, inspiring them to pursue excellence and innovation. Dr. Krishna Kumar Kishor, Vice Principal, offered felicitations.

Course completion certificates were distributed to the graduating students, followed by the oath-taking ceremony led by Mr. Sunder V., HoD, AIML. The programme concluded with the vote of thanks by Dr. Binoy Balan K., HoD, Science & Humanities, the National Anthem, and department-wise group photographs.







IEEE Circuits And Systems Society Student Branch Chapter Inaugurated



IEEE CIRCUITS AND SYSTEMS SOCIETY STUDENT BRANCH CHAPTER — ASET CHAPTER INAUGURATED



DATE
Friday, 10 July 2026

VENUE
Visvesvaraya Hall, ASET

INAUGURATED BY
Dr. P. R. Suresh, Principal, ASET

CHIEF GUEST
Dr. Arun A. V., Vice Chair – IEEE CAS Kerala Chapter,
Asst. Professor, Dept. of ECE, TKM College of Engineering, Kollam

The IEEE Circuits and Systems (CAS) Student Branch Chapter was formally inaugurated to strengthen research, innovation and hands-on learning in circuits, systems and VLSI design. The chapter aims to build the core technical skills students need to secure placements in leading VLSI and semiconductor companies.

IEEE CAS STUDENT BRANCH CHAPTER | ASET
Ahalia Campus, Palakkad • www.ahalia.ac.in

FIFA Fest 2026

The Department of Physical Education, in association with the ASET NSS Unit, organized FIFA Fest 2026 on 10.07.2026 at Ahalia Campus. The event featured football matches between Argentina and Norway and Portugal and Brazil fans. Based on the results, Argentina and Portugal fans advanced to the final. The final match was played in an exciting atmosphere, and Team Portugal emerged as the winners. The programme was a grand success, promoting sportsmanship, teamwork, and a healthy competitive spirit among students.



Alumni Talk “From Campus to Career: My Journey as a Software Engineer.”

The Department of Computer Science and Engineering, Ahalia School of Engineering & Technology, successfully organized an Alumni Talk on 13 July 2026 titled “From Campus to Career: My Journey as a Software Engineer.”

The session was delivered by our distinguished alumnus Mr. Aswin Unnikrishnan (2019–2023 Batch, CSE), Software Engineer-II, Dell Technologies, Bangalore.

Mr. Aswin shared his inspiring journey from being a CSE student at ASET to building a successful career in the software industry. He provided valuable insights into campus placements, technical skill development, interview preparation, workplace expectations, and career growth in the IT sector. The interactive session motivated students and clarified many of their career-related queries.



Two-day Workshop – Bridge Course On Mathematics For Machine Learning

The Bridge Course on Mathematics for Machine Learning was successfully conducted on 7th and 8th July 2026 for the S3 students of the Department of Artificial Intelligence and Machine Learning (AIML). The primary objective of the programme was to strengthen the students' mathematical foundation, enabling them to better understand the fundamental concepts of Machine Learning and its practical applications.

The bridge course was organized in the S3 AIML Classroom and featured sessions on essential mathematical topics, including Matrices and their Applications in AI/ML, Linear Regression, Principal Component Analysis (PCA), Graph Adjacency Matrices, and Markov Transition Matrices. Special emphasis was given to

demonstrating the relevance of these concepts in Machine Learning through practical examples and problem-solving exercises. One of the key sessions, titled “Matrices in AI/ML – Solved Examples,” illustrated the role of matrix operations in neural networks, eigenvalue-based stability analysis, recommender systems, and other AI applications.

The programme was handled by eminent faculty members from the Department of Science and Humanities, namely Dr. Binoy Balan K., Professor & Head, Department of Science and Humanities, along with Ms. Prabha R., Ms. Sangeetha P. S., Ms. Preeja V., and Ms. Sunitha K. G., Assistant Professors. Their expertise and interactive teaching methodology enabled students to understand complex mathematical concepts with ease through lectures, worked examples, discussions, and problem-solving sessions.

Students actively participated throughout the programme by engaging in discussions, solving numerical problems, and clarifying their doubts. The sessions effectively bridged the gap between theoretical mathematics and its practical application in Artificial Intelligence and Machine Learning, thereby enhancing students’ confidence in approaching core Machine Learning topics.

The programme was conducted under the guidance of Mr. Sunder V., Head of the Department, AI&ML, whose encouragement and support contributed significantly to the successful organization of the event. The programme was coordinated by Mrs. Reshma P. K., Assistant Professor, Department of AI&ML, who was responsible for the overall planning, coordination, and smooth execution of the bridge course.

Overall, the Bridge Course on Mathematics for Machine Learning achieved its intended objectives by providing students with a strong mathematical foundation essential for their upcoming Machine Learning courses. The enthusiastic participation of the students, combined with the dedicated efforts of the resource persons and organizers, made the programme a meaningful and successful academic initiative.



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DEPARTMENT OF ARTIFICIAL INTELLIGENCE & MACHINE LEARNING

BRIDGE COURSE ON MATHEMATICS FOR MACHINE LEARNING
A two-day mathematical foundation program for machine learning

07 08 2026
Reshma P.K. Head, Dept.

Strengthening the mathematical foundation essential for understanding machine learning – building analytical and problem-solving skills for emerging technological fields.

RESOURCE PERSONS

Dr. Binoy Balan K., Professor & Head, Dept.
Prabha R., Assistant Professor, Dept.
Sangeetha P.S., Assistant Professor, Dept.
Preeja V., Assistant Professor, Dept.
Sunitha K.G., Assistant Professor, Dept.

PROGRAM COORDINATOR
Sunder V., HOD, AI&ML

COORDINATOR
Reshma P.K., Assistant Professor, Dept.

STUDENT COORDINATOR
Anandhu Krishna S.S., HOD, AI&ML
Muthu Nisha M.S., HOD, AI&ML



Bridging Civil Engineering and Embedded Systems: Arduino Workshop Conducted

The Department of Civil Engineering, in association with the Department of Electronics and Communication Engineering (ECE), Ahalia School of Engineering and Technology, successfully organized a One-Day Hands-on Workshop on "Arduino for Civil Engineers" on 17 July 2026 at the Design & Analysis Lab.

The workshop was conducted with the objective of introducing Civil Engineering students to the fundamentals of Arduino-based embedded systems and their growing applications in modern civil engineering. The programme highlighted the role of smart technologies in infrastructure development, structural health monitoring, environmental sensing, and automation.

The technical sessions were led by Mr. Vinod Kumar P, Assistant Professor, ECE, Mr. Sathyan P, Senior Instructor, ECE, and Mr. Sreejesh R, Teaching Assistant, ECE. The resource persons provided comprehensive training on Arduino hardware, Arduino IDE, basic programming, sensor interfacing, data acquisition, and real-time monitoring systems.

During the hands-on sessions, participants gained practical experience in assembling Arduino circuits, interfacing sensors, writing and uploading programs, and developing simple prototype applications relevant to civil engineering. The workshop enabled students to understand how embedded systems can be integrated into smart infrastructure and monitoring applications.

The programme witnessed enthusiastic participation from Civil Engineering students, who actively engaged in the practical activities and technical discussions. The workshop successfully bridged the gap between civil engineering and emerging digital technologies, encouraging students to explore innovative solutions for sustainable and intelligent infrastructure.

The Department of Civil Engineering extends its sincere appreciation to the Department of Electronics and Communication Engineering and all the resource persons for their valuable support in making the workshop a great success.



Karmadeep 2026 Internship Program

The 30-day internship program, jointly organized by Zendalona and Ahalia School of Engineering and Technology, ran from June 1 to July 2, 2026, and brought together student presentations, laboratory-based technical work, and a structured series of interactions with placed alumni and industry-connected resource persons. The internship continues in a remote format for a further five months beyond the on-campus phase, allowing interns to keep contributing to live projects after returning to their regular academic schedules. Beyond its technical component, the program's central emphasis was on strengthening the placement pipeline for the college, connecting interns directly with individuals who had recently secured positions at reputed companies and using their first-hand experiences to prepare the current batch for upcoming placement drives. Interns were not merely spectators in this process: they were given real, production-grade open-source work to contribute to, under the mentorship of Zendalona's development team, so that the technical and placement-oriented halves of the program reinforced one another. This report documents the venues and facilities used, the nature of the technical work undertaken by the interns, the day-wise flow of activities, and, in particular, the resource persons and placement-related interactions that took place over the course of the internship. Four venues were used across the duration of the internship, each suited to the particular nature of the sessions held there. Vishweshvaraya Hall served as the venue for Days 1 and 2, and again on June 16 and 17, hosting the inaugural sessions, industry talks, and larger presentations; it was equipped with microphone, projector, and TV/display facilities suitable for addressing the full batch of interns at once. Lab 3 was the primary venue for the bulk of the internship, in use from June 3 onward. It was equipped with Wi-Fi, Ethernet connectivity, computer systems, and speaker facilities, and served as the base for the daily presentations, coding sessions, and interactive discussions that formed the backbone of the program. Lab 1 was used for the concluding two days of the internship, July 1 and 2, providing a change of setting for the final presentations and wrap-up activities. The ASET Boardroom, meanwhile, was reserved for more focused, smaller-group interactions: placement talks, group discussions, the mock interview session, and the industrial-facilities talk delivered by Sam Thomas. The technical core of the internship was built around live, real-world open-source projects maintained by Zendalona, an organization dedicated to building free and open-source accessibility software for persons with visual impairment. Rather than working through simulated exercises, interns were onboarded directly onto Zendalona's active codebases, so that their contributions had a genuine chance of shipping to real users.



Three Days Hands-on Workshop on Cadence and OrCAD

Objectives of the Activity

To introduce students to the complete Analog and Digital IC Design flow using Cadence tools.

To provide hands-on exposure to industry-standard EDA tools used in VLSI design.

To familiarize students with RTL synthesis, circuit simulation, schematic capture and physical layout design.

To enhance students' practical skills and prepare them for higher studies and careers in VLSI and semiconductor industries.

C. PO/PSO Mapping

PO1 – Engineering Knowledge

PO2 – Problem Analysis

PO3 – Design/Development of Solutions

PO5 – Engineering Tool Usage

PO11 – Life-long Learning

PSO1 (Level 3): Develop electronics-based solutions for real-life challenges integrating sustainable practices.

PSO2 (Level 2): Uphold ethics and values in designing sustainable technologies while embracing lifelong learning for professional growth.

D. Activity Description

The Department of Electronics and Communication Engineering organized a Three-Day Hands-on Workshop on Cadence and OrCAD from 22 July 2026 to 24 July 2026. Mr. B.R. Rakesh, Application Engineer, Trellisign, Bangalore, served as the resource person. The first two days focused on Analog and Digital IC Design flow, including RTL-to-GDSII implementation, Cadence Genus for synthesis, Spectre for analog simulation, and layout design. The third day covered OrCAD PCB design with hands-on sessions on schematic capture, PCB layout, routing, design rule checking, and manufacturing file generation. Students gained practical exposure to industry-standard EDA tools.

E. Learning Outcomes Achieved

Explain the complete Analog and Digital IC Design flow.

Apply Cadence Genus and Spectre tools for synthesis and simulation.

Analyse the RTL-to-GDSII implementation process.

Design basic PCB layouts using OrCAD.

Utilize industry-standard EDA tools for projects and research.

Feedback Summary:

The programme received an excellent response from the participants. Students appreciated the relevance of the content, the effectiveness of the resource person, and the extensive hands-on sessions. The workshop improved their understanding of Cadence and OrCAD tools and increased their confidence in applying modern EDA tools for academic and career development.

DEPARTMENT OF
ELECTRONICS & COMMUNICATION ENGINEERING

In Association with

CAS
VLSI CIRCUITS AND
SYSTEMS SOCIETY

IEEE CASS
ASET CHAPTER

Three-Day Hands-on Training Programme on

CADENCE & ORCAD

ADVANCED EDA TOOLS FOR VLSI

RESOURCE PERSON
Rakesh BR
Application Engineer
Trellisign Technologies,
Bangalore

DATE
July 22nd to
July 24th 2026

TIME
9.30 AM

VENUE
VLSI Lab

WHAT YOU WILL LEARN

1
SCHEMATIC
DESIGN

2
RTL TO GDS

3
LAYOUT
DESIGN

4
DRC AND LVS

5
ANALOG DIGITAL
DESIGN

6
ORCAD DESIGN
FOR ANALOG CIRCUIT

Department of Electronics and Communication Engineering
Ahalia School of Engineering and Technology



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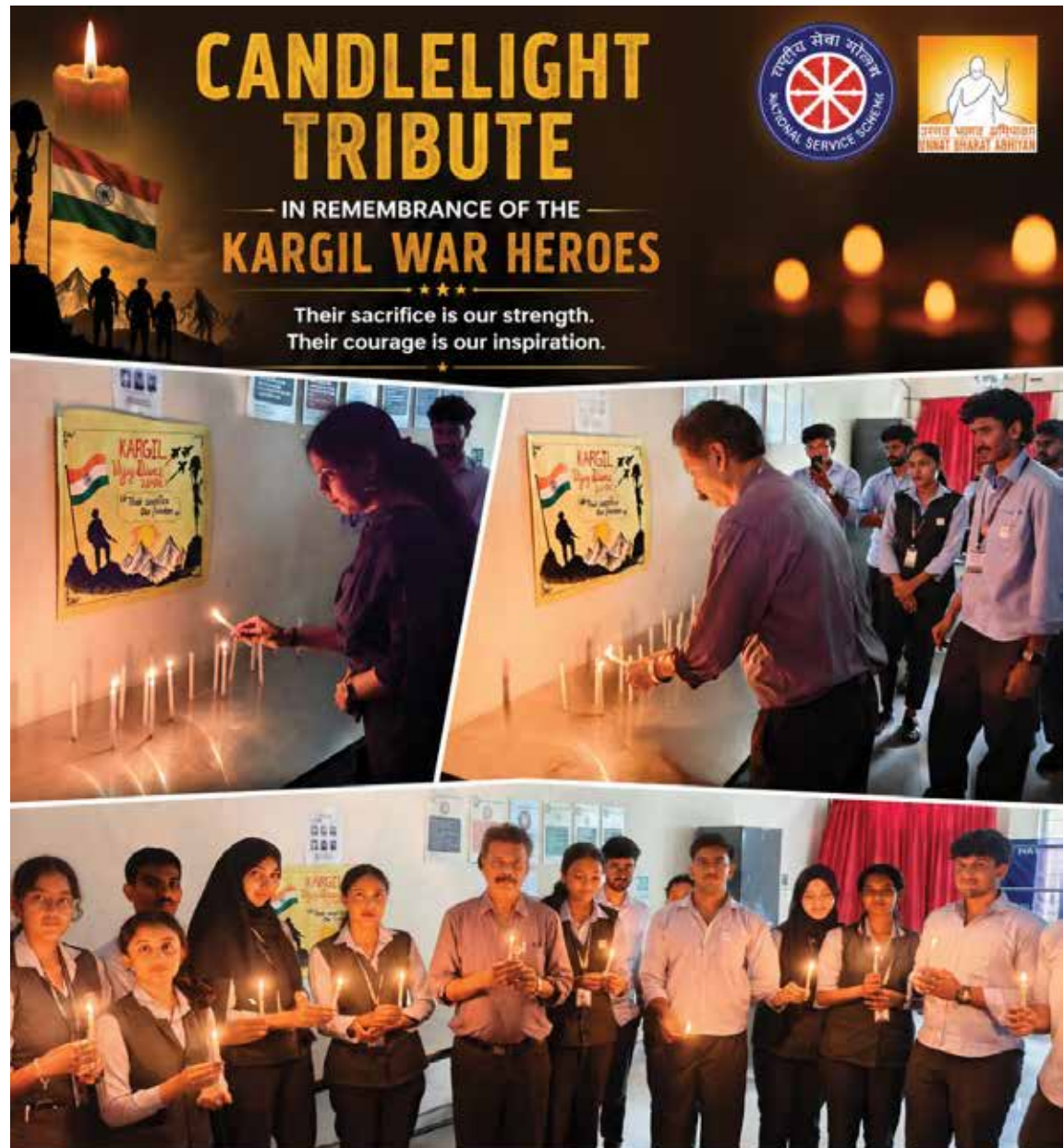
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NSS ACTIVITIES

Candlelight Tribute

The UBA Cell and NSS Unit No. 311 of Ahalia School of Engineering & Technology (ASET), Palakkad, organised a Candlelight Tribute in remembrance of the Kargil War Heroes on 27 July 2026 at the NSS Room.

The programme was inaugurated by the Principal, Dr. P. R. Suresh, who paid tribute to the brave soldiers and highlighted the importance of patriotism, courage, and selfless service. Faculty members, NSS volunteers, UBA Cell members, and students participated in the event by lighting candles and observing a moment of silence to honour the martyrs. The programme concluded with a renewed sense of patriotism and gratitude towards the Indian Armed Forces.



The ASET NSSUnit 311 organized Sanjeevanam 9.0, a Voluntary Blood Donation Camp, in collaboration with the Ahalia Blood Bank on 24.07.2026 at 9.00 AM in Seminar Hall 2, Ahalia School of Engineering and Technology. The programme aimed to promote voluntary blood donation and create awareness about its importance in saving lives. The campwitnessed active participation from students, faculty, and staff, who came forward to donate blood with a sense of social responsibility. The medical team from Ahalia Blood Bank conducted the donor screening and blood collection process, ensuring the safety and well-being of all donors throughout the camp. The programme was successfully conducted and received an excellent response from the participants. It helped spread awareness about the value of regular voluntary blood donation and encouraged students to contribute towards society through this life-saving initiative.



MOU WITH INDUSTRIES

MoU Signing Ceremony

The Department of Computer Science and Engineering, Ahalia School of Engineering & Technology, signed a Memorandum of Understanding (MoU) with digri.ai, Coimbatore to strengthen academia-industry collaboration.

The MoU was signed in the presence of Dr. P. R. Suresh, Principal, Dr. Gunasekaran S, Head of the Department of Computer Science and Engineering, Dr. Reshmi B Associate Professor- CSE, Mr. Vasanth G, Co-Founder Gyan Matrix Academy Private Limited and all the faculty members of the Department of Computer Science and Engineering.

This collaboration aims to provide students with enhanced opportunities for workshops, Professional Training & Capacity Building Programs, internships, industrial training, Skill development for students through digital platform, Profile Assessment tools and to bridge the gap between academics and industry.



ASET Signs MoU with Arcite School of Technical Education

Ahalia School of Engineering & Technology (ASET), Palakkad, signed a Memorandum of Understanding (MoU) with Arcite School of Technical Education on 22 July 2026. The partnership, facilitated by the Industry-Institute Interaction Cell (IIIC), ASET, aims to strengthen industry-academia collaboration through skill development programmes, technical training, internships, expert talks, and collaborative initiatives, thereby enhancing the employability and professional growth of students.

The MoU signing was attended by Dr. P. R. Suresh (Principal), Dr. Vivek Gopi (IIIC Coordinator), Dr. B. V. Mathew (Head, Department of Civil Engineering), Dr. Murugananth G. (Head, Department of Electrical & Electronics Engineering), Dr. Gunasekharan (Head, Department of Computer Science & Engineering), Dr. Aneesh (Head, Department of Electronics & Communication Engineering), along with officials from Arcite School of Technical Education. Together towards empowering future


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MEMORANDUM OF UNDERSTANDING (MoU)
22-07-2026




Ahalia School of Engineering & Technology
 &

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SCHOOL OF TECHNICAL EDUCATION


Industry-Institute Interaction Cell (IIIC), ASET

MoU signing ceremony between ASET and Zendalona

The Industry-Institute Interaction Cell (IIIC) of Ahalia School of Engineering & Technology (ASET), Palakkad, organized the signing of a Memorandum of Understanding (MoU) with Zendalona on 20th July 2026. The MoU was formally signed by Dr. P. R. Suresh, Principal, Ahalia School of Engineering & Technology, and Dr. Salini Sathya-sheelan, President, Zendalona. The ceremony was attended by faculty members, IIIC representatives, and officials from Zendalona. The primary objective of the MoU is to strengthen industry-academia collaboration by facilitating student internships, industrial training, live projects, expert talks, FDPs, collaborative research, innovation initiatives, and placement opportunities. Through this partnership, students will gain valuable industry exposure and practical skills, while both organizations will work together to promote knowledge sharing and technological advancement. This collaboration reflects ASET's commitment to enhancing the employability of its students by fostering strong relationships with leading industry partners and creating a dynamic ecosystem for academic and professional excellence through the Industry-Institute Interaction Cell (IIIC).



MoU signing ceremony between ASET and TeckiDEZ Pvt. Ltd., Palakkad

On 13 July 2026, Ahalia School of Engineering & Technology (ASET), under the guidance of the Industry Institute Interaction Cell (IIIC), signed a Memorandum of Understanding (MoU) with TeckiDEZ Pvt. Ltd., Palakkad. The collaboration aims to strengthen academia-industry interaction by creating opportunities for students and faculty in the areas of technical training, internships, live projects, industrial visits, skill development, research, and placement support. The MoU was formally signed by Dr. P. R. Suresh, Principal, ASET, and representatives of TeckiDEZ Pvt. Ltd., in the presence of Dr. Rajeev, Dean Academics, Dr. Bhavin K Bharath, HOD- ME and Dr. Vivek Gopi, Coordinator- IIIC. The event included the exchange of the signed agreement, followed by discussions on future collaborative activities and knowledge-sharing initiatives. The partnership marks another significant step in ASET's continued efforts to bridge the gap between academia and industry, ensuring that students gain practical exposure and industry-relevant competencies.



2026
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Monday & Tuesday

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